
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Vincent Hool, et al.

Attorney Docket No.:
ALTRP099/A1197

Application No.: Please assign

Examiner: Please assign

Filed: Herewith

Group: Please assign

Title: MODULATED FLIP CHIP SUBSTRATE
DESIGN

CERTIFICATE OF EXPRESS MAILING

I hereby certify that this paper and the documents and/or fees referred to as attached therein are being deposited with the United States Postal Service on February 19, 2004 in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR §1.10, Mailing Label Number **EV334019185US** addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signed: _____

Jeffrey Ng

**INFORMATION DISCLOSURE STATEMENT
37 CFR §§1.56 AND 1.97(b)**

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure

Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. ALTRP099)

Respectfully submitted,
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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No.	Application No.:
	ALTRP099/A1197	NEW
	Applicant:	
	Hool, et al.	
	Filing Date	Group
	HEREWITH	UNASSIGNED

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A1						
	A2						
	A3						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B1							
	B2							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C1	Zuken Be First, "Hot-Stage 4.1", www.zuken.com
	C2	Cadence, "PCB Design Studio, Cadence PCB Design Tools and the Latest Technology at a Breakthrough Price", 2003 Cadence Design Systems, Inc., www.cadence.com
	C3	Cadence, "PCB Design Expert, High-Speed, Constraint-Driven PCB Design", 2003 Cadence Design Systems, Inc., www.cadence.com
	C4	Mentor Graphics, "Destiny RE for Cadence Allegro", 2003 Mentor Graphics Corporation
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.